



WASHINGTON STATE  
UNIVERSITY

# Dual-Purpose Hardware Algorithms and Architecture: Part I – Floating-Point Division

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ARITH 2023(Sep4-6, 2023)

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# Motivation

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- Data dependency: Two examples in
  - GNU linear programming kit (GLPK)
  - GNU Scientific library (GSL)

$$X = s \cdot \tan^{-1} \left[ \frac{\{a \cdot \sqrt{b \cdot c} \cdot \cos(d - e) + f \cdot \sqrt{g \cdot h} \cdot \cos(i - j)\} \cdot k}{l \cdot (m + n) + p \cdot (q + r)} \right]$$

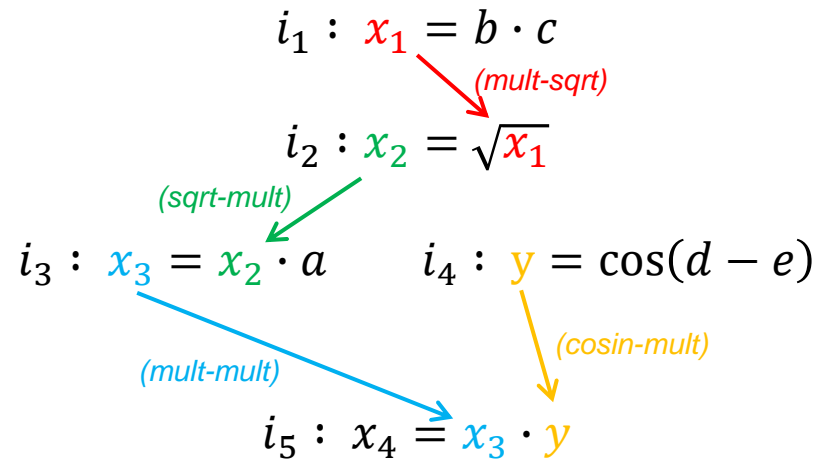
$$Y = \sqrt{\frac{a}{b}} \cdot (c \cdot \cos x + d \cdot e \cdot \sin y)$$

# Motivation

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$$X = s \cdot \tan^{-1} \left[ \frac{\{a \cdot \sqrt{b \cdot c} \cdot \cos(d - e) + f \cdot \sqrt{g \cdot h} \cdot \cos(i - j)\} \cdot k}{l \cdot (m + n) + p \cdot (q + r)} \right]$$

$$a \cdot \sqrt{b \cdot c} \cdot \cos(d - e) \longrightarrow$$

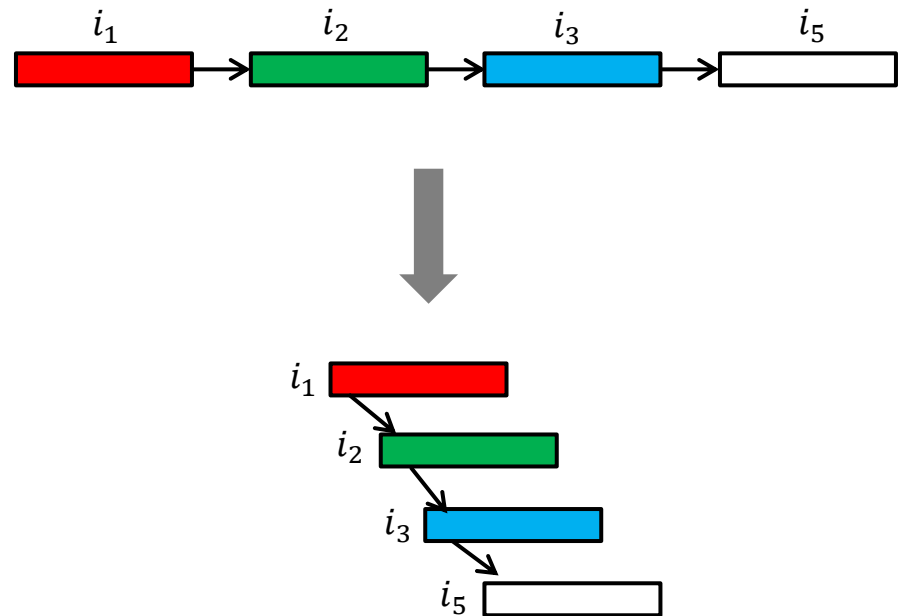


# Motivation

- Resolving dependency chain :  $i_1 \rightarrow i_2 \rightarrow i_3 \rightarrow i_5$

$$\begin{aligned} i_1 : x_1 &= b \cdot c \\ i_2 : x_2 &= \sqrt{x_1} \\ i_3 : x_3 &= x_2 \cdot a \\ i_4 : y &= \cos(d - e) \\ i_5 : x_4 &= x_3 \cdot y \end{aligned}$$

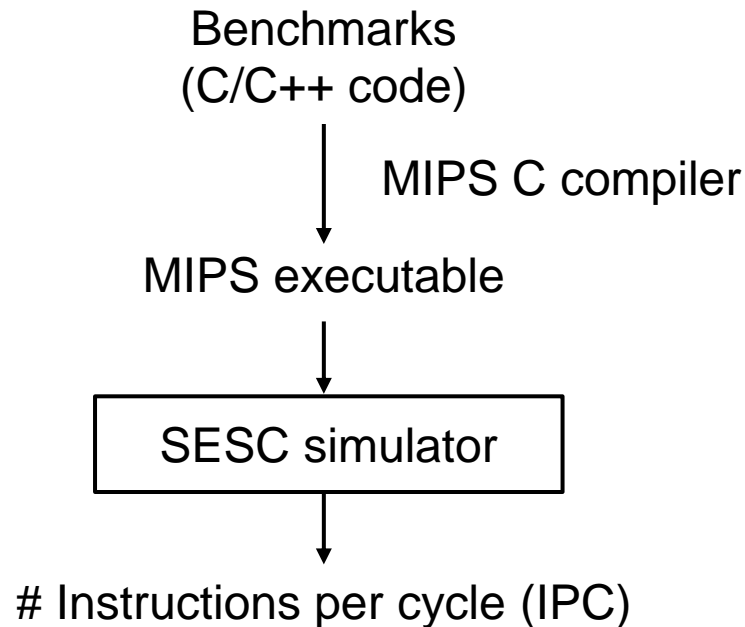
Diagram illustrating the dependency chain resolution. The equations are connected by arrows: a red arrow from  $x_1$  in  $i_1$  to  $x_1$  in  $i_2$ ; a green arrow from  $x_2$  in  $i_2$  to  $x_2$  in  $i_3$ ; a dashed grey arrow from  $x_3$  in  $i_3$  to  $x_3$  in  $i_5$ ; and a dashed grey arrow from  $y$  in  $i_4$  to  $y$  in  $i_5$ .



# Architecture Simulation

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- Simulator
  - SESC (cycle-accurate MIPS32 simulator)



# Architecture Simulation

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- Benchmarks
  - B1: Finite element method (FEM)
  - B2: Electromagnetic (EM) simulation
  - B3: Statistical inference
  - B4: Numerical solver of the Bessel function
- # clock cycles

	# Cycles
Integer ALU	1
Integer Mult	4
Integer Div	15
FP Add/Sub	4
FP Mult	6
FP Div	16

# Architecture Simulation

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- Profiling results (# instructions)

	B1	B2	B3	B4
Arithmetic	59.8%	62.8%	74.8%	73.8%
Non-Arithmetic	40.2%	37.2%	25.2%	26.2%
Integer ALU	43%	35%	38%	29%
Integer Mult & Div	0.1%	0%	0%	0%
Branch / Jump	8.0%	8.2%	11.2%	8.1%
Load / Store	32.2%	29.0%	14.0%	28.1%
FP Add/Sub	11.4%	17.5%	27.7%	22.7%
FP Mult	4.6%	9.7%	8.9%	11.3%
FP Div	0.7%	0.6%	0.2%	0.8%

	# Cycles
Integer ALU	1
Integer Mult	4
Integer Div	15
FP ALU	4
FP Mult	6
FP Div	16

B1 : Finite element method (FEM)

B2 : EM simulation

B3 : Statistical inference

B4 : Bessel function



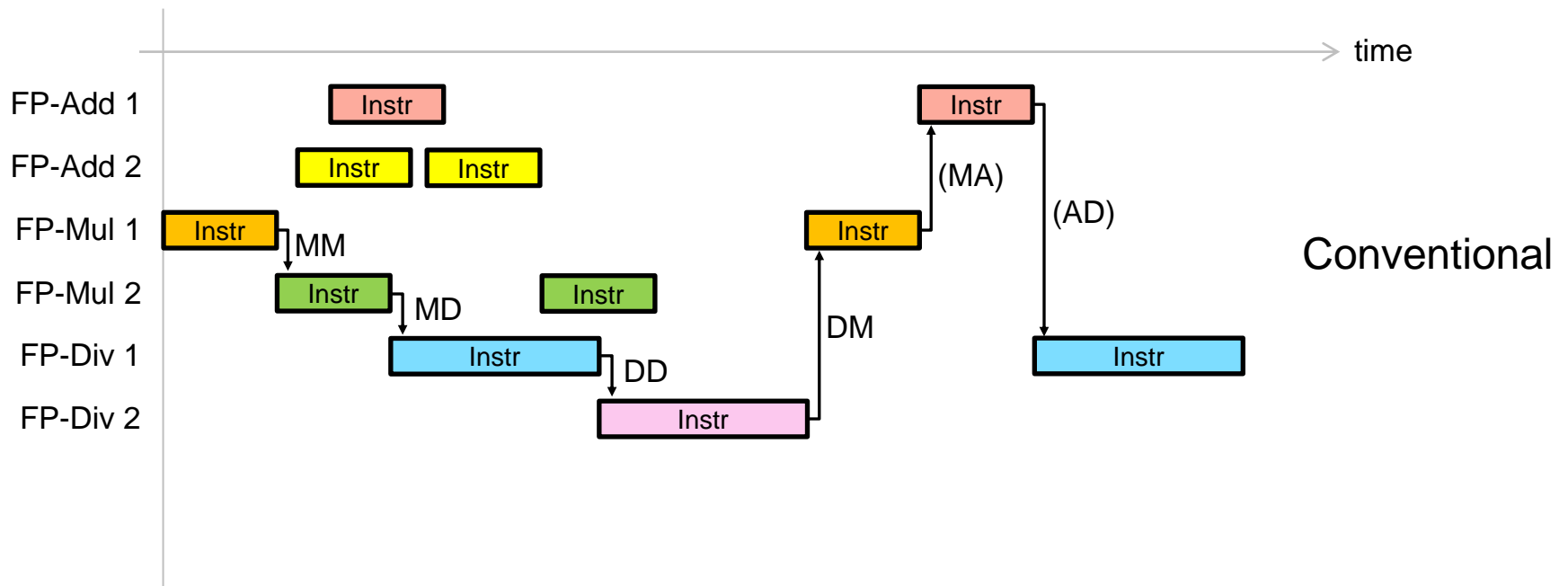
# Architecture Simulation

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- Dependency types
  - **M**ultiplication  $\rightarrow$  **M**ultiplication (MM)
    - Ex:  $(a \times b) \times c$  or  $c \times (a \times b)$
  - **M**ultiplication  $\rightarrow$  **D**ivision (MD)
    - Ex:  $\frac{(a \times b)}{c}$  or  $\frac{c}{(a \times b)}$
  - **D**ivision  $\rightarrow$  **M**ultiplication (DM)
    - Ex:  $\left(\frac{a}{b}\right) \times c$  or  $c \times \left(\frac{a}{b}\right)$
  - **D**ivision  $\rightarrow$  **D**ivision (DD)
    - Ex:  $\frac{\left(\frac{a}{b}\right)}{c}$  or  $\frac{c}{\left(\frac{a}{b}\right)}$

# Architecture Simulation

- Resolve dependencies

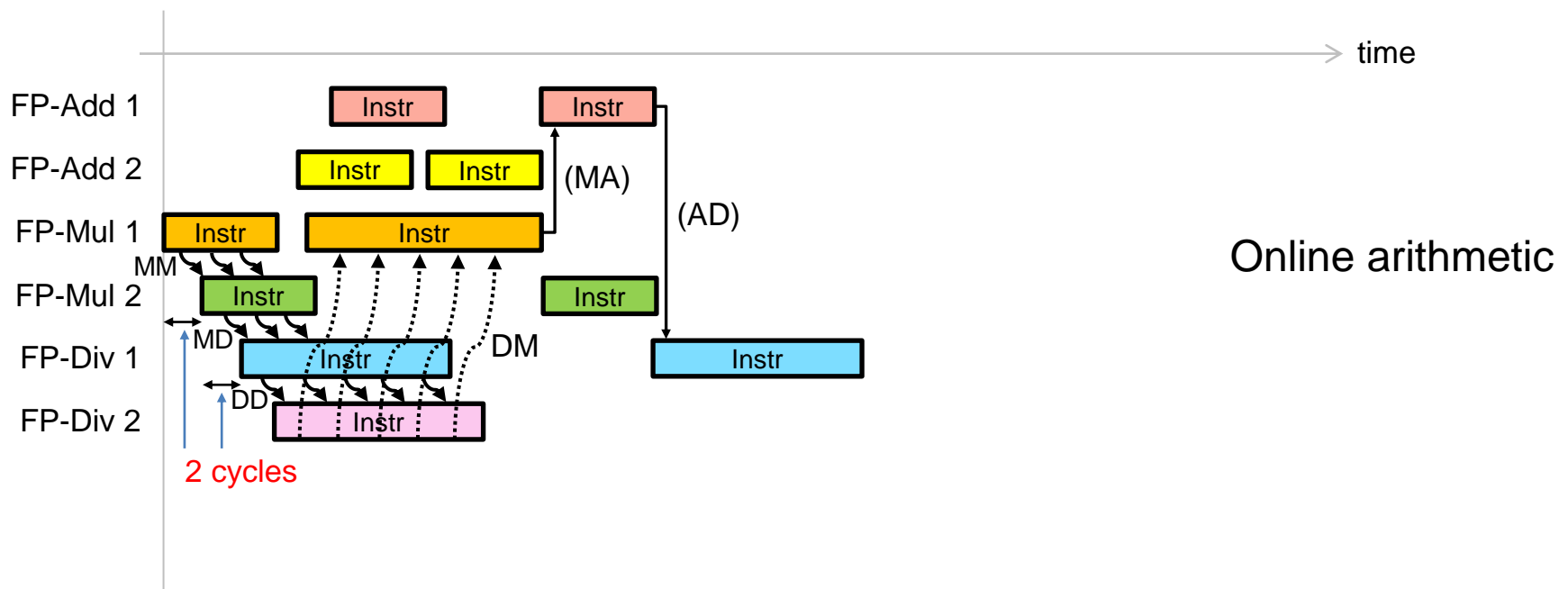


\* Not drawn to scale.

\* Other instructions are not shown.

# Architecture Simulation

- Resolve dependencies (simulation of ideal cases)
  - If there is a dependency between two arithmetic instructions (multiplications/divisions), then we assume that we can start the second instruction **two cycles** after the first instruction is launched.



\* Not drawn to scale.

\* Other instructions are not shown.

# Architecture Simulation

- Reduction of the total # cycles (%)

$$(a \times b) \times c$$

or

$$c \times (a \times b)$$

$$\frac{(a \times b)}{c}$$

or

$\frac{c}{c}$

$$\frac{(a \times b)}{(a \times b)}$$

$$\left(\frac{a}{b}\right) \times c$$

or

$$c \times \left(\frac{a}{b}\right)$$

$$\frac{\left(\frac{a}{b}\right)}{c} \text{ or } \frac{c}{\left(\frac{a}{b}\right)}$$

	MM (M→M)	MD (M→D)	DM (D→M)	DD (D→D)	Total (%)
B1	0	0.58	3.85	0.36	4.79
B2	5.71	0	0	0	5.71
B3	0.56	0.25	0.33	0.43	1.57
B4	23.61	0	1.47	2.96	28.04

B1 : Finite element method (FEM)

B2 : EM simulation

B3 : Statistical inference

B4 : Bessel function

# Offline Division: Digit-Recurrence (Review)

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- Notation (floating-point)
  - Quotient obtained down to the  $j$ -th digit

$$q[j] = \underbrace{q_0 \cdot q_1 q_2 \dots q_j}_{\text{obtained}} (\underbrace{\text{xx} \dots \text{x}}_{\text{to be obtained}}) = \sum_{i=0}^j q_i \cdot r^{-i}$$

- Digit-recurrence division
  - Division:  $x = d \cdot q + rem$
  - Condition:  $|rem| < |d| \cdot ulp$
  - At  $j$ -th iteration

$$rem[j] = x - d \cdot q[j]$$

$$\begin{aligned} 0 &\leq rem[j] < d \cdot r^{-j} \\ \Leftrightarrow 0 &\leq x - d \cdot q[j] < d \cdot r^{-j} \\ \Leftrightarrow 0 &\leq \underbrace{r^j \cdot (x - d \cdot q[j])}_{w[j]} < d \end{aligned}$$

# Offline Division: Digit-Recurrence (Review)

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- Digit-recurrence division
  - At  $j$ -th iteration

$$0 \leq \underbrace{r^j \cdot (x - d \cdot q[j])}_{w[j]} < d$$

- At  $(j + 1)$ -th iteration

$$0 \leq w[j + 1] < d$$

$$q[j + 1] = \underbrace{q_0 \cdot q_1 \dots q_j}_{\text{obtained}} \overbrace{q_{j+1}}^{\text{Find}} (xx \dots x)$$

$$\Leftrightarrow 0 \leq r \cdot w[j] - d \cdot q_{j+1} < d$$

- $q_{j+1} = k$  if

$$0 \leq r \cdot w[j] - d \cdot k < d$$

$$\Leftrightarrow \boxed{k \cdot d \leq r \cdot w[j] < (k + 1) \cdot d}$$

Radix- $r$  digit-recurrence division

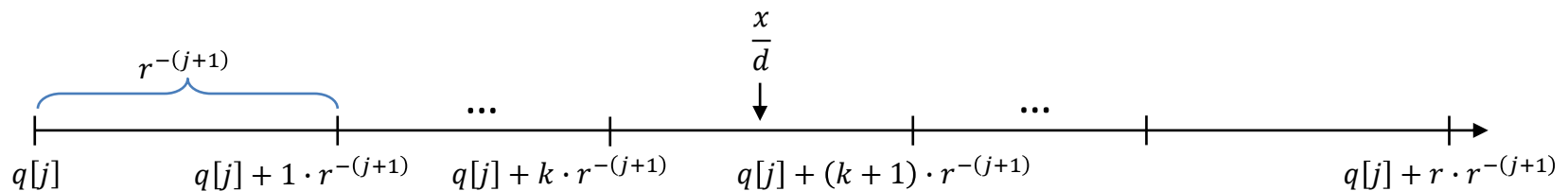
# Offline Division: Interval Analysis

- Interval-analysis-based division
  - Quotient obtained down to the  $j$ -th digit

$$q[j] = \underbrace{q_0 \cdot q_1 q_2 \dots q_j}_{\text{obtained}} \underbrace{(\text{xx} \dots \text{x})}_{\text{to be obtained}} = \sum_{i=0}^j q_i \cdot r^{-i}$$

- Condition for  $q_{j+1} = k$

$$q[j+1] = \underbrace{q_0 \cdot q_1 \dots q_j}_{\text{obtained}} \overbrace{q_{j+1}}^{\text{Find}} (\text{xx} \dots \text{x})$$



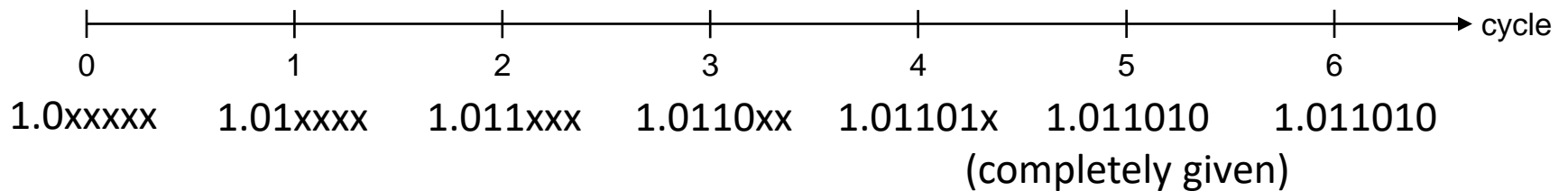
$$q[j] + k \cdot r^{-(j+1)} \leq \frac{x}{d} < q[j] + (k+1) \cdot r^{-(j+1)}$$

$$\Leftrightarrow k \cdot d \leq r^{j+1} \cdot (x - d \cdot q[j]) < (k+1) \cdot d$$

# Online division: Review

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- Operands
  - Given one digit per cycle.
  - Example:  $x = 1.011010_2$  (the leading 1 is hidden)



- Principles
  - Find the result with incomplete operands.



# Online Division: Notation

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- Input (given until the  $j$ -th iteration)

– Dividend:  $x[j] = \underbrace{x_0 \cdot x_1 \dots x_{a[j]}}_{\text{given}} (\underbrace{xx \dots x}_{\text{to be given}}) = \sum_{i=0}^{a[j]} x_i \cdot r^{-i}$

– Divisor:  $d[j] = \underbrace{d_0 \cdot d_1 \dots d_{b[j]}}_{\text{given}} (\underbrace{xx \dots x}_{\text{to be given}}) = \sum_{i=0}^{b[j]} d_i \cdot r^{-i}$

- Output (obtained until the  $j$ -th iteration)

– Quotient:  $q[j] = \underbrace{q_0 \cdot q_1 \dots q_{c[j]}}_{\text{obtained}} (\underbrace{xx \dots x}_{\text{to be obtained}}) = \sum_{i=0}^{c[j]} q_i \cdot r^{-i}$

# Online Division

- Special case (most of the other papers)

- $a[j] = j$ : a digit of  $x$  is given every cycle.

- $x[j] = x_0.x_1 \dots x_{a[j]}(\text{xx} \dots \text{x}) = x_0.x_1 \dots x_j(\text{xx} \dots \text{x})$

- $b[j] = j$ : a digit of  $d$  is given every cycle.

- $d[j] = d_0.d_1 \dots d_{b[j]}(\text{xx} \dots \text{x}) = d_0.d_1 \dots d_j(\text{xx} \dots \text{x})$

	0	1	2	3	4	5	6
$x$	1.1xxxxx	1.11xxxx	1.110xxx	1.1101xx	1.11010x	1.110101 (fully given)	1.110101
$d$	1.0xxxxx	1.00xxxx	1.001xxx	1.0011xx	1.00111x	1.001110 (fully given)	1.001110

- General case (our work)

- An arbitrary number of new digits of  $x$  and  $d$  could be given every cycle.

	0	1	2	3	4	5	6
$x$	1.110xxx	1.110xxx	1.110xxx	1.110101 (fully given)	1.110101	1.110101	1.110101
$d$	1.0xxxxx	1.00111x	1.00111x	1.00111x	1.00111x	1.001110 (fully given)	1.001110

# Online Division: Interval Analysis

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- Given

- $x[j] = x_0.x_1 \dots x_{a[j]}(\text{xx} \dots \text{x})$

- $x_{\text{MIN}} = x_0.x_1 \dots x_{a[j]}0 \dots 0$  (padded 0's)

- $x_{\text{MAX}} = x_0.x_1 \dots x_{a[j]}1 \dots 1$  (padded 1's)

- $d[j] = d_0.d_1 \dots d_{b[j]}(\text{xx} \dots \text{x})$

- $d_{\text{MIN}} = d_0.d_1 \dots d_{b[j]}0 \dots 0$

- $d_{\text{MAX}} = d_0.d_1 \dots d_{b[j]}1 \dots 1$

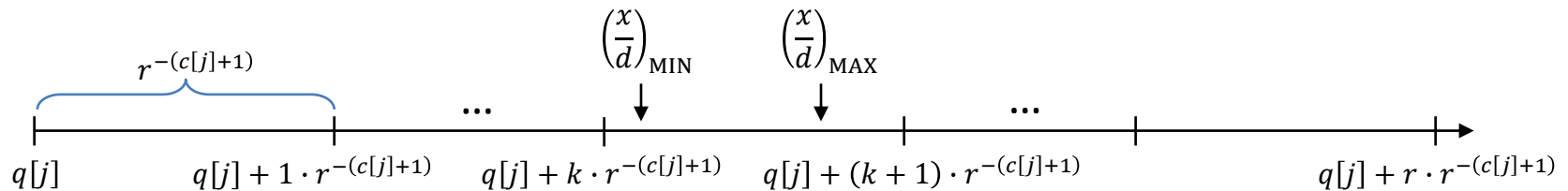
- Range of  $\frac{x}{d} = \left[ \left(\frac{x}{d}\right)_{\text{MIN}}, \left(\frac{x}{d}\right)_{\text{MAX}} \right]$

- $\left(\frac{x}{d}\right)_{\text{MIN}} = \frac{x_{\text{MIN}}}{d_{\text{MAX}}} = \frac{x_0.x_1 \dots x_{a[j]}0 \dots 0}{d_0.d_1 \dots d_{b[j]}1 \dots 1}$

- $\left(\frac{x}{d}\right)_{\text{MAX}} = \frac{x_{\text{MAX}}}{d_{\text{MIN}}} = \frac{x_0.x_1 \dots x_{a[j]}1 \dots 1}{d_0.d_1 \dots d_{b[j]}0 \dots 0}$

# Online Division: Interval Analysis

- Obtained
  - $q[j] = q_0 \cdot q_1 \dots q_{c[j]} (\underline{x} \dots \underline{x})$
- Find  $q_{c[j]+1}$ 
  - $q_{c[j]+1} = k$  if



– This just means

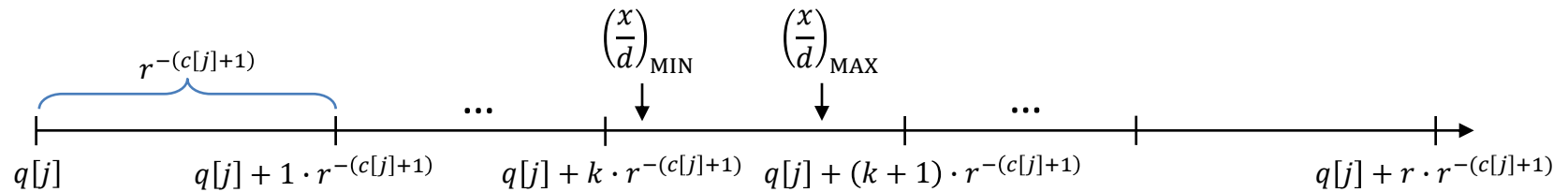
- $q_0 \cdot q_1 \dots q_{c[j]} k 0 \dots 0 \leq \frac{x}{d} < q_0 \cdot q_1 \dots q_{c[j]} (k+1) 0 \dots 0$
- which means  $q[j] = q_0 \cdot q_1 \dots q_{c[j]} k \dots$

– For example, for  $r = 4$

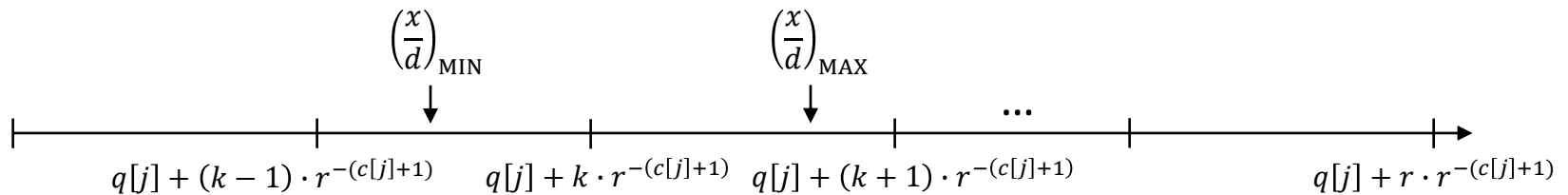
- $\underbrace{1.321\underline{2}}_{\text{obtained}} \dots \leq \frac{x}{d} < \underbrace{1.321\underline{3}}_{\text{obtained}} \dots \Rightarrow \frac{x}{d} = 0321\underline{2} \dots$   
 $q_{-(c[j]+1)} = 2$

# Online Division: Interval Analysis

- Case 1:  $q_{-(c[j]+1)} = k$



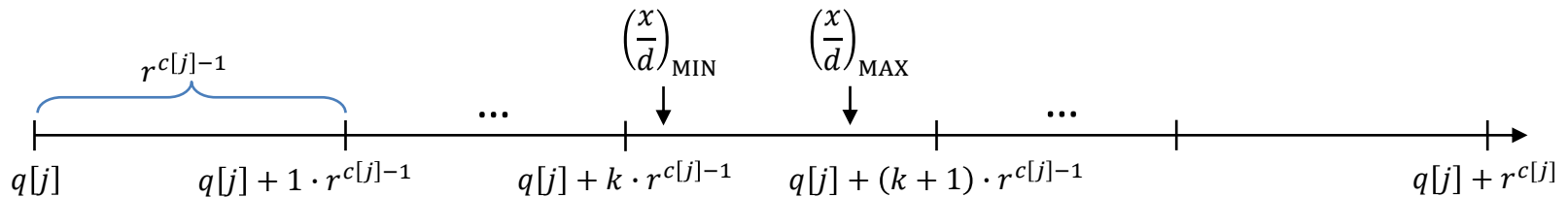
- Case 2:  $q_{c[j]+1} = ?$



- In the example above,  $q_{c[j]+1}$  could be  $k-1$  or  $k$ .
- We need more digits of  $x$  and/or  $d$  to find  $q_{c[j]+1}$ .

# Online Division: Interval Analysis

- Formula: Two inequalities



$$\text{Ineq\#1) } q[j] + k \cdot r^{c[j]-1} \leq \left(\frac{x}{d}\right)_{\text{MIN}}$$

$$\rightarrow q[j] + k \cdot r^{c[j]-1} \leq \frac{x_{n-1}x_{n-2}\dots x_{a[j]}0\dots 0}{d_{n-1}d_{n-2}\dots d_{b[j]}1\dots 1} = \frac{x[j]}{d[j] + r^{b[j]-1}}$$

$$\text{Ineq\#2) } \left(\frac{x}{d}\right)_{\text{MAX}} < q[j] + (k+1) \cdot r^{c[j]-1}$$

$$\rightarrow \frac{x_{n-1}x_{n-2}\dots x_{a[j]}1\dots 1}{d_{n-1}d_{n-2}\dots d_{b[j]}0\dots 0} = \frac{x[j] + r^{a[j]-1}}{d[j]} < q[j] + (k+1) \cdot r^{c[j]-1}$$

# Online Division: Interval Analysis

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$$1. \quad q[j] + k \cdot r^{c[j]-1} \leq \frac{x[j]}{d[j] + r^{b[j]-1}} \quad (\text{Solve it for } k = 0, 1, \dots, r-1)$$

$$\Leftrightarrow (q[j] + k \cdot r^{c[j]-1}) \cdot (d[j] + r^{b[j]-1}) \leq x[j] \quad \text{Inequality 1}$$

$$2. \quad \frac{x[j] + r^{a[j]-1}}{d[j]} < q[j] + (k+1) \cdot r^{c[j]-1} \quad (\text{Solve it for } k = 0, 1, \dots, r-1)$$

$$\Leftrightarrow x[j] + r^{a[j]-1} < d[j] \cdot (q[j] + (k+1) \cdot r^{c[j]-1}) \quad \text{Inequality 2}$$

- What if  $x$  and  $d$  are offline (fully given at time 0)?

– Then,  $a[j] = b[j] = 0$  for all  $j$  and  $c[j] = n - j$ .

$$1. \quad (q[j] + k \cdot r^{n-j-1}) \cdot d[j] \leq x[j]$$

$$2. \quad x[j] < d[j] \cdot (q[j] + (k+1) \cdot r^{n-j-1})$$

$$\Rightarrow k \cdot d \leq r^{j+1-n} \cdot (x - d \cdot q[j]) < (k+1) \cdot d$$

Radix- $r$  digit-recurrence division

In this case, the online division algorithm is equivalent to the offline division algorithm. (dual-purpose)

# Online Division: Interval Analysis

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- Example (decimal numbers)

- $x$  (dividend): 1.8036
- $d$  (divisor): 1.2631
- $q$  (quotient): x.xxxx

- Assumption

- One digits of  $x$  and  $d$  are given every cycle.
- We **try** to obtain one digit of the quotient.

- Division

- $x: 1.8xxx, d: 1.2xxx$   $1.38 \dots \leq \left(\frac{x}{d}\right)_{MIN} = \frac{1.8000}{1.2999} \leq \left(\frac{x}{d}\right)_{MAX} = \frac{1.8999}{1.2000} \leq 1.58 \dots \rightarrow q = 1.xxxx$
- $x: 1.80xx, d: 1.26xx$   $1.43 \dots \leq \left(\frac{x}{d}\right)_{MIN} = \frac{1.8000}{1.2699} \leq \left(\frac{x}{d}\right)_{MAX} = \frac{1.8099}{1.2600} \leq 1.43 \dots \rightarrow q = 1.4xxx$
- $x: 1.803x, d: 1.263x$   $1.426 \dots \leq \left(\frac{x}{d}\right)_{MIN} = \frac{1.8030}{1.2639} \leq \left(\frac{x}{d}\right)_{MAX} = \frac{1.8039}{1.2630} \leq 1.428 \dots \rightarrow q = 1.42xx$
- $x: 1.8036, d: 1.2631$   $1.4279 \leq \left(\frac{x}{d}\right)_{MIN} = \frac{1.8036}{1.2631} \leq \left(\frac{x}{d}\right)_{MAX} = \frac{1.8036}{1.2631} \leq 1.4279\dots \rightarrow q = 1.427x$
- Find the next quotient digit.  $1.4279 \leq \left(\frac{x}{d}\right)_{MIN} = \frac{1.8036}{1.2631} \leq \left(\frac{x}{d}\right)_{MAX} = \frac{1.8036}{1.2631} \leq 1.4279\dots \rightarrow q = 1.4279$



# Online Division: Interval Analysis

- Implementation example (binary, radix-4)

– given

- $x$  (dividend): 1.110101101101xxxx
- $d$  (divisor): 1.011011110001xxxx

– we have obtained

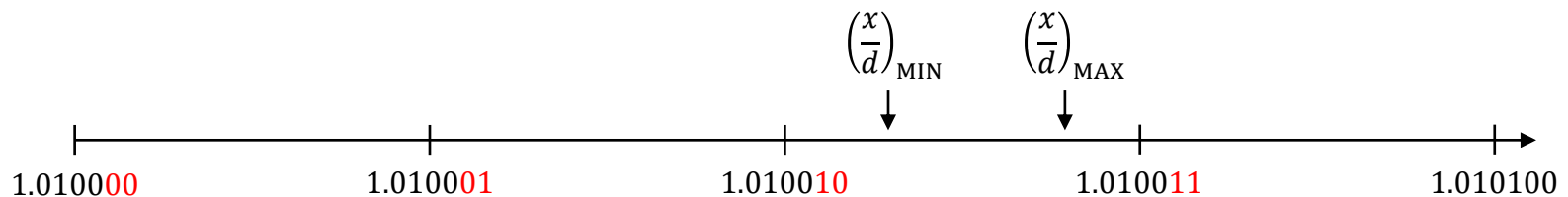
- $q$  (quotient): 1.0100xxxxxxxxxxxx

Find

- Solve

k	Inequality 1	Inequality 2
0	$(q + 0 \cdot 4^{-(2+1)}) \cdot (d + 4^{-6} - 4^{-8}) \leq x$ (True)	$x + 4^{-6} - 4^{-8} < d \cdot (q + 1 \cdot 4^{-(2+1)})$ (False)
1	$(q + 1 \cdot 4^{-(2+1)}) \cdot (d + 4^{-6} - 4^{-8}) \leq x$ (True)	$x + 4^{-6} - 4^{-8} < d \cdot (q + 2 \cdot 4^{-(2+1)})$ (False)
2	$(q + 2 \cdot 4^{-(2+1)}) \cdot (d + 4^{-6} - 4^{-8}) \leq x$ (True)	$x + 4^{-6} - 4^{-8} < d \cdot (q + 3 \cdot 4^{-(2+1)})$ (True)
3	$(q + 3 \cdot 4^{-(2+1)}) \cdot (d + 4^{-6} - 4^{-8}) \leq x$ (False)	$x + 4^{-6} - 4^{-8} < d \cdot (q + 4 \cdot 4^{-(2+1)})$ (True)

– Thus,  $q_5q_6 = k = 2$  (10)  $\rightarrow q=1.0100$ **10**xxxxxxxx



# Online Division: Interval Analysis

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## 1. Inequality 1

$$(q[j] + k \cdot r^{-(c[j]+1)}) \cdot (d[j] + r^{-b[j]} - ulp) \leq x[j]$$

$$\Leftrightarrow \underbrace{q[j] \cdot d[j]}_m + \underbrace{q[j] \cdot r^{-b[j]}}_{\text{shift } q[j]} - \underbrace{q[j] \cdot ulp}_{\text{shift } q[j]} + \underbrace{k \cdot d[j] \cdot r^{-(c[j]+1)}}_{\text{shift } k \cdot d[j]} + \underbrace{k \cdot d[j] \cdot r^{-(b[j]+c[j]+1)}}_{\text{shift } k \cdot d[j]} - \underbrace{k \cdot r^{-(c[j]+1)} \cdot ulp}_{\text{shift } k} \leq x[j]$$

## 2. Inequality 2

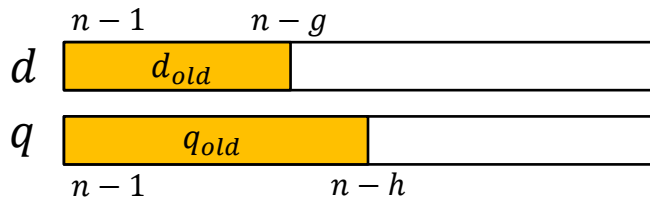
$$x[j] + r^{-a[j]} - ulp < d[j] \cdot (q[j] + (k + 1) \cdot r^{-(c[j]+1)})$$

$$\Leftrightarrow x[j] + r^{-a[j]} - ulp < \underbrace{q[j] \cdot d[j]}_m + \underbrace{(k + 1) \cdot d[j] \cdot r^{-(c[j]+1)}}_{\text{shift } (k + 1) \cdot d[j]}$$

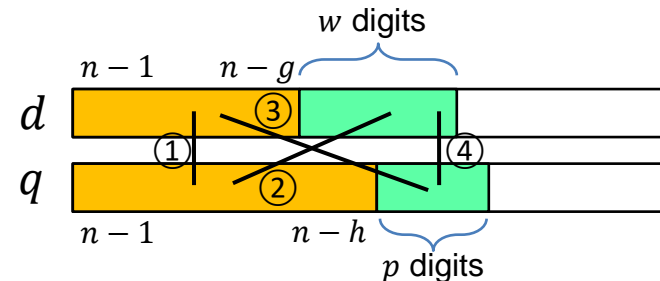
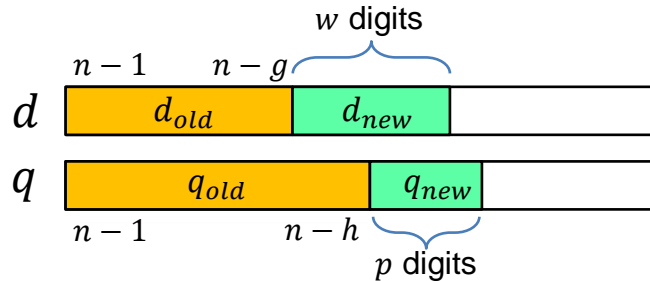
- $m[g, h] = (d_0 \cdot d_1 \dots d_g 0 \dots 0) \cdot (q_0 \cdot q_1 \dots q_h 0 \dots 0)$ 
  - We can incrementally update  $m[g, h]$ .

# Online Division: Interval Analysis

- Incremental update of  $m[g, h] = (d_{n-1} \dots d_{n-g} 0 \dots 0) \cdot (q_{n-1} \dots q_{n-h} 0 \dots 0)$ .
  - Suppose we have obtained  $m[g, h]$ .



- A few more digits are given ( $d$ :  $w$  more digits.  $q$ :  $p$  more digits).



$$- m[g + w, h + p] = m[g, h] \dots \textcircled{1} d_{old} * q_{old}$$

$$+ (d_{n-g-1:n-g-w} \cdot r^{n-g-w} \cdot q_{n-1:n-h} \cdot r^{n-h}) \dots \textcircled{2} d_{new} * q_{old}$$

$$+ (d_{n-1:n-g} \cdot r^{n-g} \cdot q_{n-h-1:n-h-p} \cdot r^{n-h-p}) \dots \textcircled{3} d_{old} * q_{new}$$

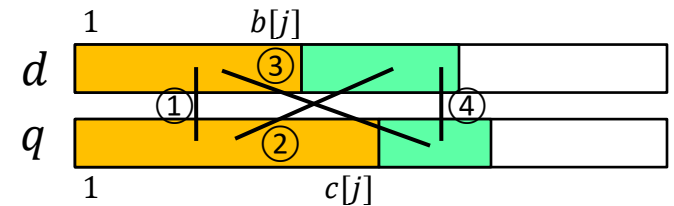
$$+ (d_{n-g-1:n-g-w} \cdot r^{n-g-w} \cdot q_{n-h-1:n-h-p} \cdot r^{n-h-p}) \dots \textcircled{4} d_{new} * q_{new}$$

If  $w$  and  $p$  are small,  
this update can be  
done in a cycle.

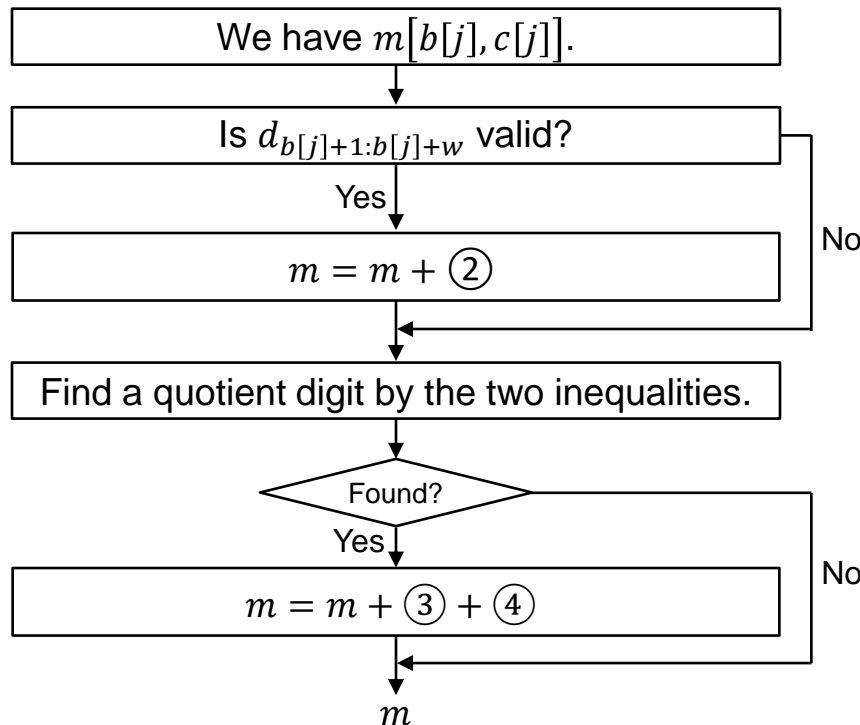
→ Use  $w, p$ : 2 and 4bit

# Online Division: Interval Analysis

- When do we perform the incremental update of  $m = d \cdot q$ ?
  - When new digits of  $d$  are given.
  - When we find new digits of  $q$ .



- Flowchart



### Four cases

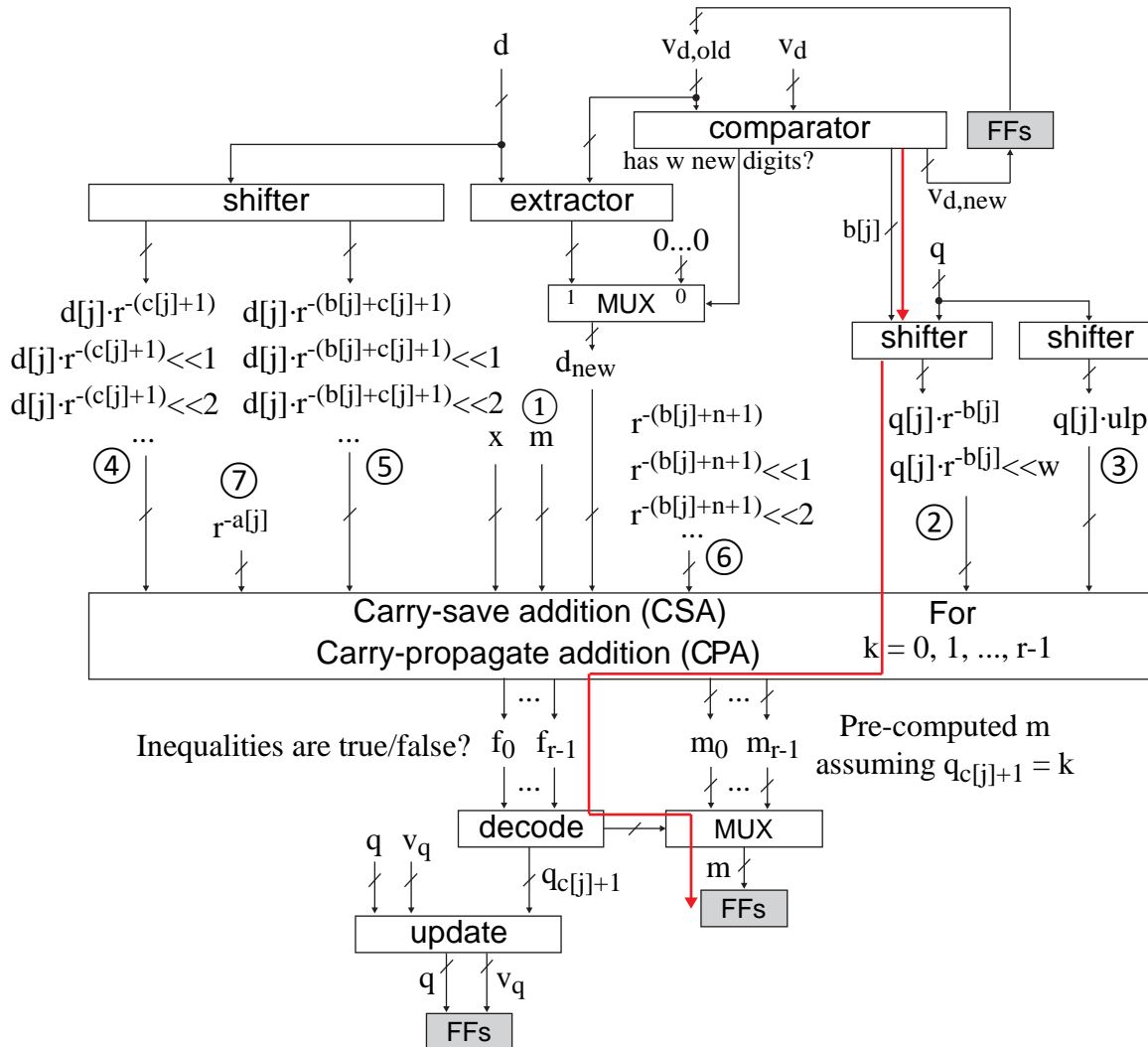
New valid digits of $d$	Found new Digit of $q$	Updated $m$
No	No	$m[b[j], c[j]]$
No	Yes	$m[b[j], c[j] + 1]$
Yes	No	$m[b[j] + w, c[j]]$
Yes	Yes	$m[b[j] + w, c[j] + 1]$

# Hardware Architecture

---

- Input
  - Dividend  $x$  and its valid bits
    - $x = x_0.x_1 \dots x_n$  ( $x_0$  is the hidden 1)
    - $v_x = v_{x_0}v_{x_1} \dots v_{x_n}$  (11...100...0)
      - $v_{x_i} = 1$  (or 0):  $x_i$  is valid (invalid)
      - The generator of  $x$  has to generate  $v_x$  too.
  - Divisor  $d$  and its valid bits
    - $d = d_0.d_1 \dots d_n$  ( $d_0$  is the hidden 1)
    - $v_d = v_{d_0}v_{d_1} \dots v_{d_n}$
- Output
  - Quotient  $q$  and its valid bits
    - $q = q_{n-1}q_{n-2} \dots q_0$
    - $v_q = v_{q_{n-1}}v_{q_{n-2}} \dots v_{q_0}$  (we generate)
- Design parameters
  - $r$ : Radix- $r$  division. Try to obtain  $\log_2 r$  bits per cycle.
  - $w$ : # unused bits of  $d$  (divisor) to update  $m = d[j] \cdot q[j]$  ( $w = 2$  or 4 bits)

# Hardware Architecture



$$\begin{aligned}
 & \textcircled{1} \quad q[j] \cdot d[j] + \textcircled{2} \quad q[j] \cdot r^{-b[j]} - \textcircled{3} \quad q[j] \cdot ulp \\
 & + k \cdot d[j] \cdot r^{-(c[j]+1)} \textcircled{4} \\
 & + k \cdot d[j] \cdot r^{-(b[j]+c[j]+1)} - k \cdot r^{-(c[j]+1)} \cdot ulp \\
 & \leq x[j] \quad \textcircled{5} \quad \textcircled{6}
 \end{aligned}$$

**Inequality 1**

---


$$\begin{aligned}
 & \textcircled{7} \\
 & x[j] + r^{-a[j]} - ulp \\
 & < q[j] \cdot d[j] + (k+1) \cdot d[j] \cdot r^{-(c[j]+1)} \\
 & \textcircled{1} \quad \textcircled{4}
 \end{aligned}$$

**Inequality 2**

For example  
 $k = 6$  (0110),  
 we add  $k \cdot d[j] \cdot r^{-(c[j]+1)}$  by adding  
 (by CSAs)

$$\begin{aligned}
 & d[j] \cdot r^{-(c[j]+1)} \ll 1 \\
 & d[j] \cdot r^{-(c[j]+1)} \ll 2
 \end{aligned}$$

# Simulation Setup

---

- **Design**
  - 64bit divider (64bit dividend & divisor)
- **Implementation**
  - Verilog
- **Synthesis**
  - Synopsys Design Compiler
  - 22nm standard cell library
- **Performance metrics**
  - Clock period, Area, Energy
  - Execution time

# Design Characteristics

	Design	Type	Radix	#divisor bits processed / cycle	Description
Others	AN16	Offline	8	-	-
	SA17	Offline	16	-	-
	JB20	Offline	64	-	ARM
	AT03	<u>Online</u>	4	2	The latest on-line divider
Ours	FF2	Offline	4	-	Floating-point <b>o</b> ffline (Interval-analysis-based, not partial-remainder-based)
	FF4	Offline	16	-	
	FN22	<u>Online</u>	4	2	Floating-point <b>o</b> nline
	FN24	<u>Online</u>	4	4	
	FN42	<u>Online</u>	16	2	
	FN44	<u>Online</u>	16	4	

- AN16 : A. Nannarelli, “Performance/Power Space Exploration for Binary64 Division Units,” in IEEE Trans. on Computers, vol. 65, no. 5, May 2016, pp. 1671–1677
- SA17 : S. Amanollahi and G. Jaberipur, “Energy-Efficient VLSI Realization of Binary64 Division with Redundant Number Systems,” in IEEE Trans. on VLSI Systems, vol. 25, no. 3, Mar. 2017, pp. 954–961
- JB20 : J. D. Bruguera, “Low Latency Floating-Point Division and Square Root Unit,” in IEEE Trans. on Computers, vol. 69, no. 2, Feb. 2020, pp. 274–287
- AT03 : A. F. Tenca, A. Shantilal, and M. Sinky, “A Radix-4 On-line Division Design and Its Application to Networks of On-line Modules,” in Proceedings of SPIE, vol. 5205, 2003, pp. 529–540



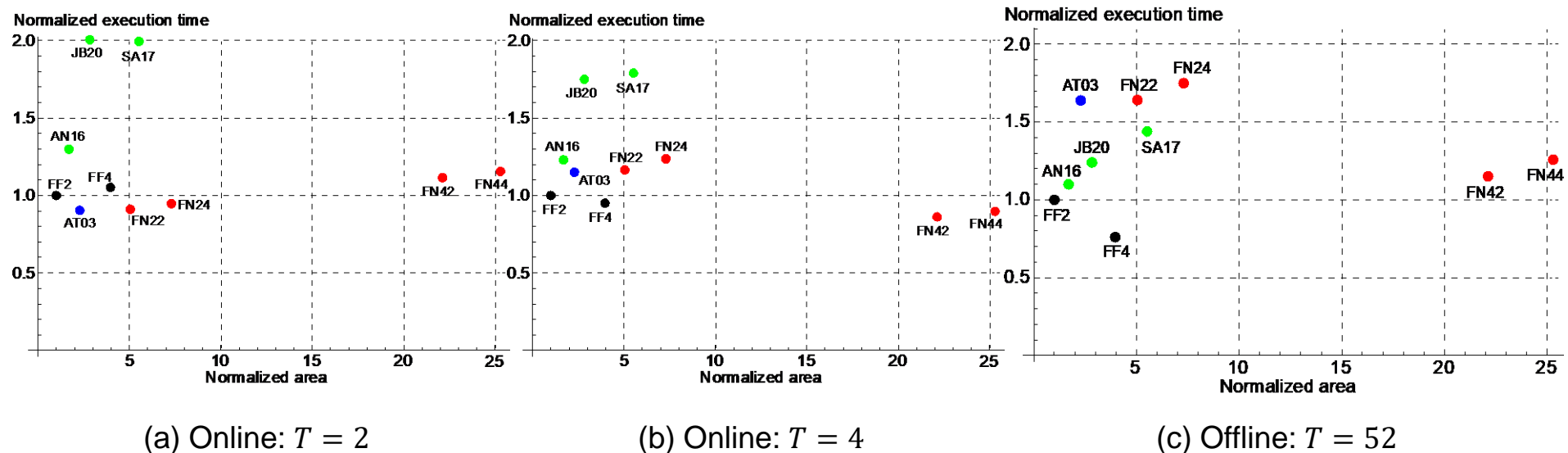
# Our Designs

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- Offline
  - FF2: Radix-4 (“2” in “FF2” means that we obtain 2 quotient bits per cycle)
  - FF4: Radix-16 (we obtain 4 quotient bits per cycle)
- Online
  - FNqd
    - q: # quotient bits to try to obtain per cycle
    - d: # divisor bits that are used to update  $m$  in a cycle
  - FN22, FN24: Radix-4
  - FN42, FN44: Radix-16




# Simulation Results

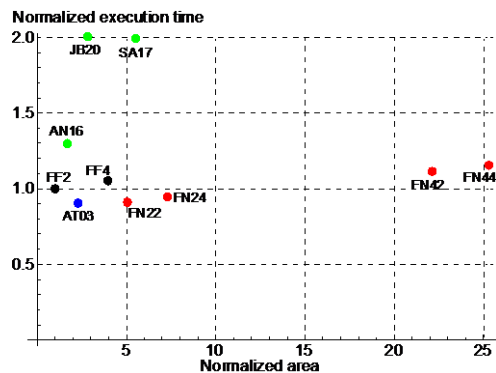
- $T$ : # bits of  $x$  and  $d$  given per cycle.
- Results: normalized to the FF2 offline divider.



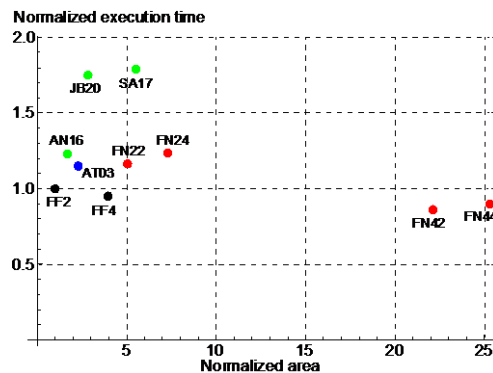
- Offline dividers: The execution time goes down (gets better) as  $T$  goes up.
- AT03, FN22, FN24: The execution time goes down as  $T$  goes up to  $T = 2$  and then saturates.
- FN42, FN44: The execution time goes down as  $T$  goes up to  $T = 4$ , then saturates.

# Simulation Results: Analysis

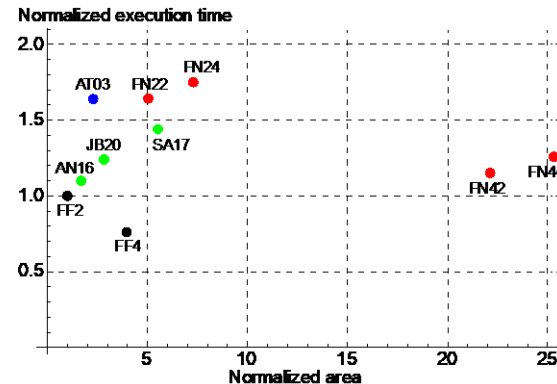
- $T$ : # bits of  $x$  and  $d$  given per cycle.
- Offline dividers: The execution time goes down as  $T$  goes up. Why?
  - Execution time: Wait time (□) + computation time (■).
  - As  $T$  goes up, the wait time goes down.
    - $T = 2$  (online): 
    - $T = 4$  (online): 
    - $T = 52$  (offline): 
- Radix-4 online dividers (AT03, FN22, FN24): saturates when  $T = 2$ .
  - If  $T < 2$ : Need more bits to find a quotient digit per cycle.
  - If  $T > 2$ : Do not process more than two bits.
- Radix-16 online dividers (FN42, FN44): saturates when  $T = 4$ .
  - If  $T < 4$ : Need more bits to find a quotient digit per cycle.
  - If  $T > 4$ : Do not process more than four bits.



(a) Online:  $T = 2$



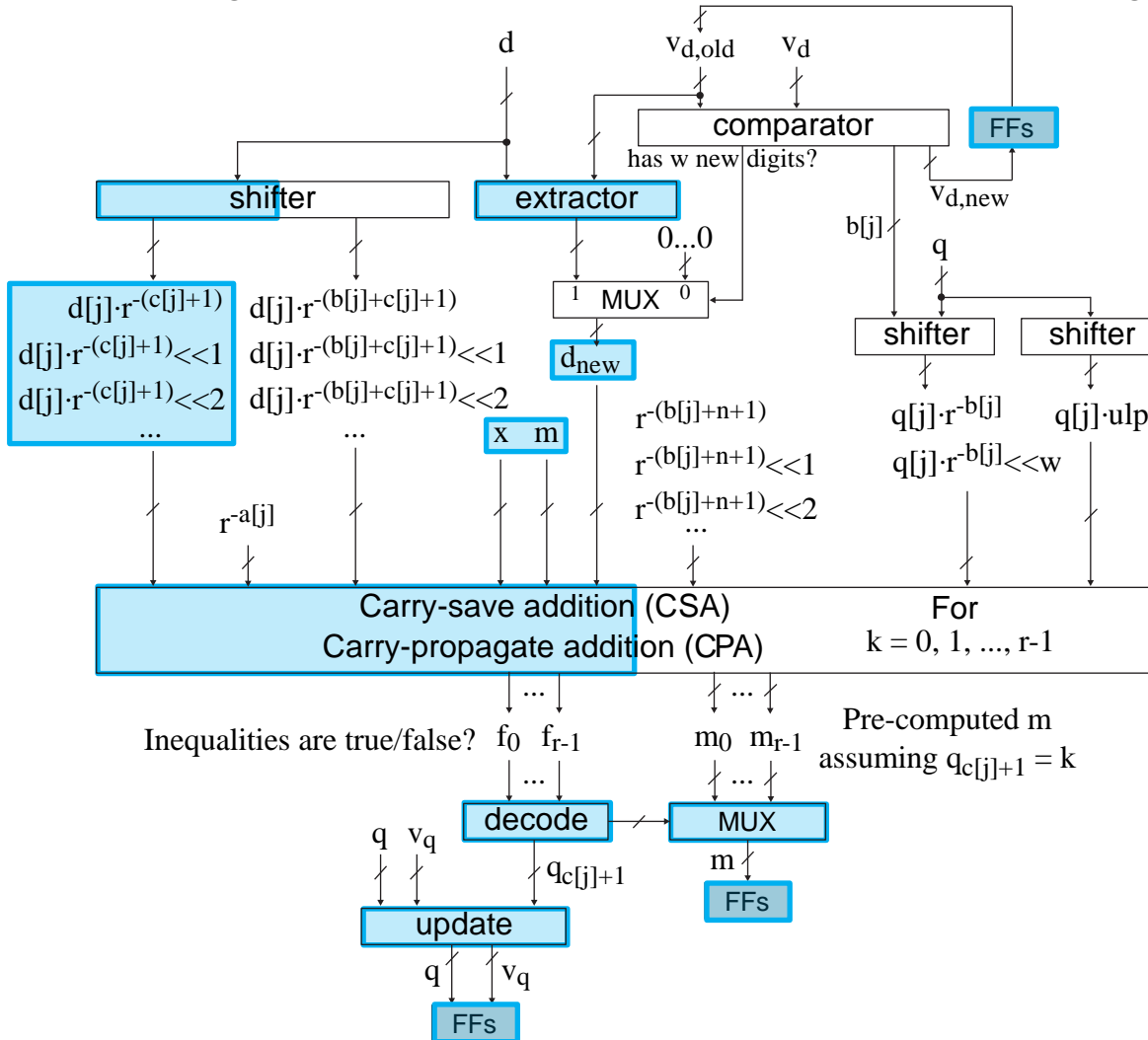
(b) Online:  $T = 4$



(c) Offline:  $T = 52$

# Discussion

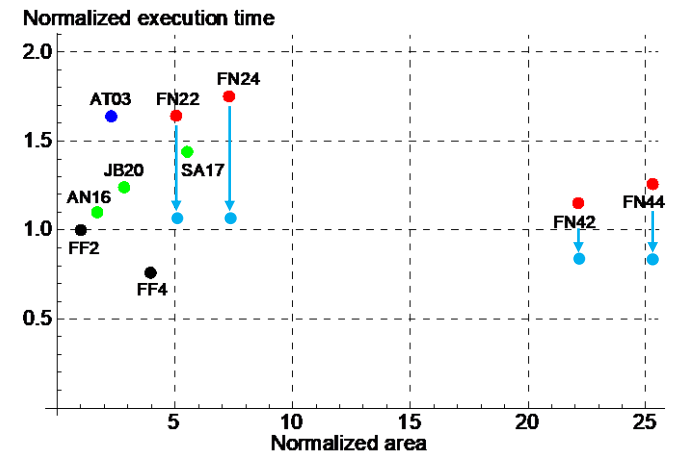
- Integration of both offline and online dividers in a single architecture



Used for offline division

If the operands are offline, then we can generate the output much more quickly with a small additional logic (bypass).

In this case, the execution time of our online dividers for offline division will be close to the execution time of the offline dividers (FF2, FF4).



(Of course, this can be applied to the integer dividers too!)

# Conclusion

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- We proposed the interval-analysis-based division algorithm, which can be used for both offline and online division.
  - Radix-4
  - Radix-16
  - Uses the normal binary number system.
- Performance (offline division)
  - Online dividers are slower than offline dividers.
    - A small modification of our online dividers could improve the performance of the FN designs significantly (close to the FF2 and FF4 designs).
- Performance (online division)
  - Online dividers outperform offline dividers.
  - Lower bit rate: AT03 outperforms our dividers.
  - Higher bit rate: Our dividers outperforms AT03.